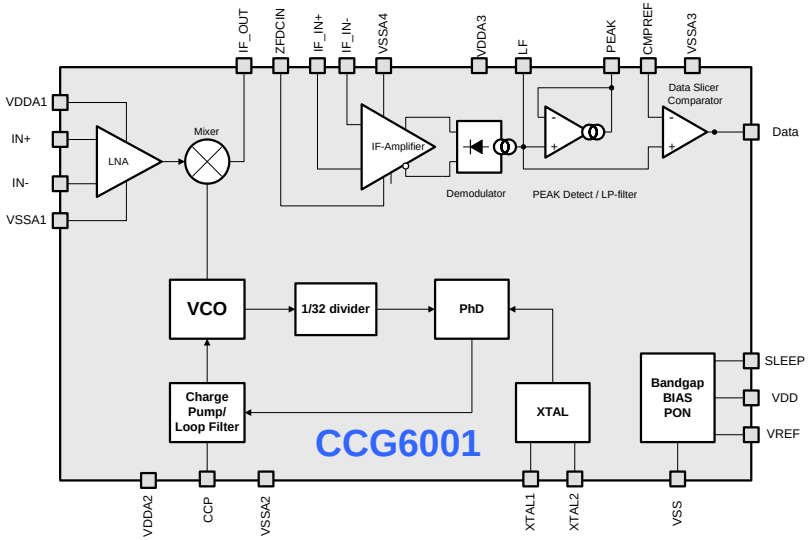
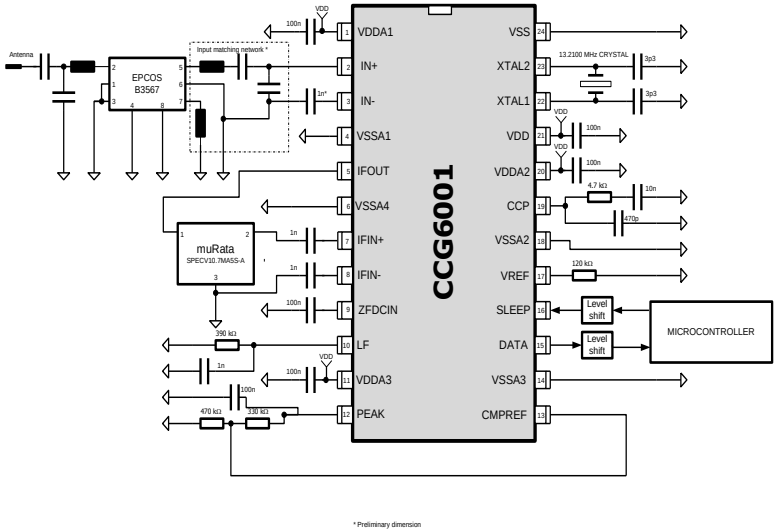
# CCG6001 433 MHz ASK Receiver

## FEATURES

- Super heterodyne ASK receiver
- high integration rate with low external components
- LNA & Mixer
- Integrated LP filter and data slicer
- Integrated bandgap reference voltage source
- Integrated PLL
- Low power consumption of typical 2.5 mA (power down mode: < 0.1 µA)
- 0.6 µm BiCMOS process
- TSSOP24 (173mil, 1.1 mm high) RoHs conform

## BLOCK DIAGRAM




* Preliminary dimension

| Parameter | Symbol | Conditions | Min. | Max. | Unit |
|---|---|---|---|---|---|
| Supply voltage | $V_{DD}$- $V_{SS}$ | | -0.70 | 6.5 | V |
| Total power dissipation | $P_{TOT}$ | @ $\vartheta_{op}$ = 85 °C | | 600*[1] | mW |
| Operation temperature range | $\vartheta_{op}$ | | -20 | 85 | °C |
| Storage temperature range | $\vartheta_{storage}$ | | -55 | 150 | °C |
| Soldering Profile | $t_{soldering}$ | $\vartheta_{sol\_max}$ = 260 °C | | 12 | s |
| ESD Protection | $V_{ESD}$ | Human Body model JEDEC JESD22 Method A114B Class2 | 2 | | kV |
| Permanent current into ESD-protection diodes | $I_{DC\_ESD}$ | Only in case of forward biased<br>ESD diodes. Input voltage above VCC or below VSS ! | | 2 | mA |
| Reliability | MTBF | (VDD –VSS) < 5.5 V | $10^5$ | | h |





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