

CLC2622 LCD Driver

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DESCRIPTION

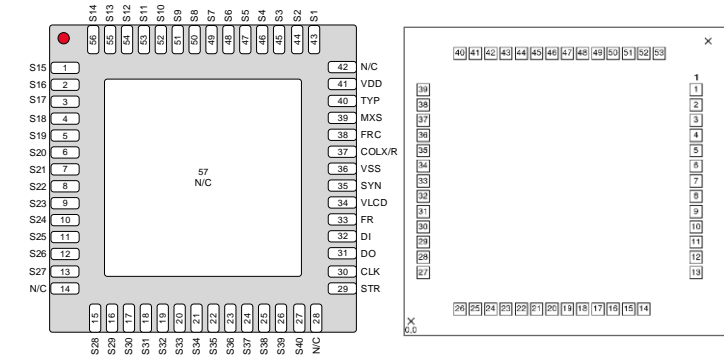
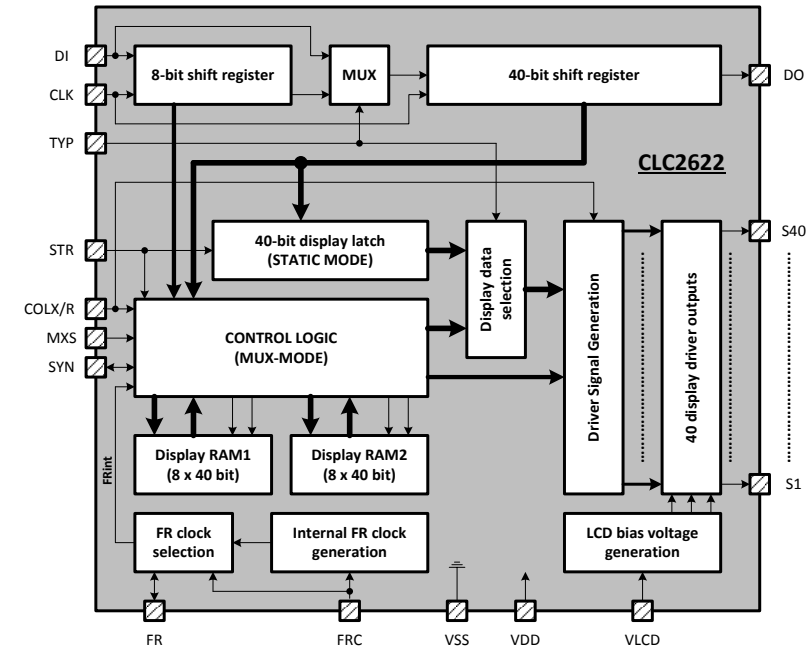
The CLC2622 is a LCD Driver with either static mode driving up to 40 segments or multiplexing mode driving up to 8 rows and 32 columns. A display storage is available via a dual RAM to be addressed by 8x40 words. The large voltage range and a low current consumption makes the driver suitable for a variety of different LCD display. Also large pixel size applications are supported due to the low driver impedance. Several CLC2622 can be easily cascaded, no external resistor bias chain and no external clock is necessary.

FEATURES

- Selectable, static- or multiplexing LCD driver
- LCD blanking by BLANK bit and STR signal, all segments on by SET bit
- On chip: LCD bias voltage generation, display refresh, internal clock generation
- Selectable synchronize signal generation for large LCD applications
- Available in a QFN 56 package or for COG bumped die (≈3x3mm, 16 μm gold bump)

ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Operation temperature range	ϑ_{op}		-40		85	°C
Logic supply voltage	V_{DD}		3		6	V
LCD supply voltage	V_{LCD}		3		9	V
LCD supply input current	I_{LCD}	All inputs Vdd, outputs open, FR ext.			250	uA
Supply current / external FR	I_{DD}	All inputs Vdd, outputs open, FR ext.		50		uA
Supply current / internal FR	I_{DD}	All inputs Vdd, outputs open, FR int.			1	mA
Driver impedance	R_{OUT}	For output drivers, $I_L = 20\mu A$		10		kΩ
FR frequency (2/4/8)	f_{FR}	FR clock external		128/256/512		Hz
FR frequency in static mode	$f_{FR-STAT}$	FR clock internal	50	100	150	Hz
FR frequency in mux mode 8	$f_{FR-MUX8}$	FR clock internal	400	800	1200	Hz



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